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**NOTICE OF APPEAL FROM THE PRIMARY EXAMINER TO  
THE BOARD OF PATENT APPEALS AND INTERFERENCES (Large Entity)**

Docket No.  
BUR919990304US1

On Re Application Of: Ference et al.

Serial No.

09/732,240

Filing Date

12/07/00

Examiner

Chris C. Chu

Group Art Unit

2815

Invention: **MULTI-CHIP STACK AND METHOD OF FABRICATION UTILIZING SELF-ALIGNING  
ELECTRICAL CONTACT ARRAY**

TO THE COMMISSIONER FOR PATENTS:

Applicant(s) hereby appeal(s) to the Board of Patent Appeals and Interferences from the decision of the Primary Examiner dated **October 2, 2003** finally rejecting Claim(s) **1-7, 10-14 and 30-35**

The fee for this Notice of Appeal is: **\$330.00**

- ☐ A check in the amount of the fee is enclosed.
- ☐ The Director has already been authorized to charge fees in this application to a Deposit Account.
- ☒ The Director is hereby authorized to charge any fees which may be required, or credit any overpayment to Deposit Account No. **09-0456 (IBM)**

*Kevin P. Radigan*  
Signature

Dated: **December 02, 2003**

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CC:

I certify that this document and fee is being deposited on **Dec. 02, 2003** with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

*Kevin P. Radigan*  
Signature of Person Mailing Correspondence

**Kevin P. Radigan**

Typed or Printed Name of Person Mailing Correspondence

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